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Date: October 3, 2001

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant

Joachim Held et al.

Applic. No.

09/839,767

Filed

April 20, 2001

Title

**Integrated Circuit** 

Art Unit

2816

## INFORMATION DISCLOSURE STATEMENT

## RECEIVED

Hon. Commissioner of Patents and Trademarks, Washington, D.C. 20231 SEP 1 9 2002

**Technology** Center 2600

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 5,677,570 (Kondoh et al.), dated October 14, 1997;

German Published, Non-Prosecuted Patent Application DE 195 40 647 A1 (Matsui et al.), dated October 24, 1996, integrated semiconductor circuit device;

German Published, Non-Prosecuted Patent Application DE 197 28 692 A1 (Pape), dated January 7, 1999, IC-component.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications,

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patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,

For Applicants

Date: October 3, 2001

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